

ART 34 AMDT

CLAIMS

1. (Amended) A signal receiving apparatus comprising a plurality of signal receiving means including:

5 input means for inputting a broadcast wave in which a video signal and/or an audio signal are modulated in a predetermined format,

 a circuit board having a circuit for selecting, from the broadcast wave, a video signal and/or an audio signal included in a predetermined frequency band and demodulating the selected signals, and a mount layer on which the circuit is mounted,
10 a first ground layer arranged, on the surface opposite to the surface on which the circuit of the mount layer is mounted, with a predetermined distance from the mount layer through a dielectric layer, and a second ground layer arranged with a predetermined distance from the first ground layer through a dielectric layer, the circuit having a microstripline structure, wherein

15 the signal receiving means are arranged such that the lowermost ground layer of one signal receiving means and the circuit mounting surface of another signal receiving means face each other, and

 the signal receiving apparatus further comprising: decode means for decoding the video signal and/or audio signal that have been selected and demodulated; and

20 output means for outputting the demodulated video signal and/or audio signal to an external device.

2. (Deleted)

3. The signal receiving apparatus according to claim 1, wherein the plurality of signal receiving means are provided for a broadcast wave.

4. The signal receiving apparatus according to claim 1, wherein the plurality of signal receiving means correspond to broadcast waves.

5. The signal receiving apparatus according to claim 1, comprising recording means for storing the video signal and/or audio signal.

6. (Deleted)

7. (Amended) A signal receiving circuit comprising: input means for inputting a broadcast wave in which a video signal and/or an audio signal are modulated in a predetermined format; and

a circuit for selecting, from the input broadcast wave, a video signal and/or an audio signal included in a predetermined frequency band and demodulating the selected signals,

wherein a mount layer on which the circuit is mounted, a first ground layer arranged on the opposite surface of the surface on which the circuit is mounted with a predetermined distance from the mount layer through a dielectric layer, and a second ground layer arranged with a predetermined distance from the first ground layer through a dielectric layer are sequentially stacked, and

the circuit has a microstripline structure.

ART 34 AMDI

8. (Deleted)

9. (Deleted)

10. (Amended) A receiver comprising; at least first and second tuners, wherein each of the first and second tuners has a double-sided printed board and

5 predetermined components, the double-sided printed board having one surface serving as components mounting surface and other surface the whole area of which serves as a ground surface,

wiring patterns of the mounting surfaces of the first and second tuners are configured to be substantially in plane-symmetry with the components mounted on the
10 respective mounting surfaces also arranged in substantially in plane-symmetry, and

the first and second tuners are arranged in the apparatus such that the ground surfaces of the respective double-sided printed boards face each other.

11. The receiver according to claim 10, wherein the each of the double-sided printed boards of the first and second tuners includes an antenna input connector, and
15 the double-sided printed board of the first and second tuners are arranged such that the antenna input connectors project through a rear-end panel of the receiver.

12. The receiver according to claim 11, wherein the first and second tuners are configured to receive digital satellite broadcasts.

13. The receiver according to claim 11, comprising a device that stores and reproduces programs of the digital satellite broadcasts.